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(54) xDSL class C-AB driver

(57) The present invention is related to a line driver for amplifying an input signal, said line driver comprising:

- a non-linear amplifier (3) arranged to provide a first output signal amplifying said input signal
- an analogue linear amplifier (5) providing a second

output signal based on the difference between the input signal and the first output signal, placed in parallel with said non-linear amplifier and being dependent thereon, and

- combining means arranged to combine said first output signal and said second output signal to provide a total output signal to an output line (7).

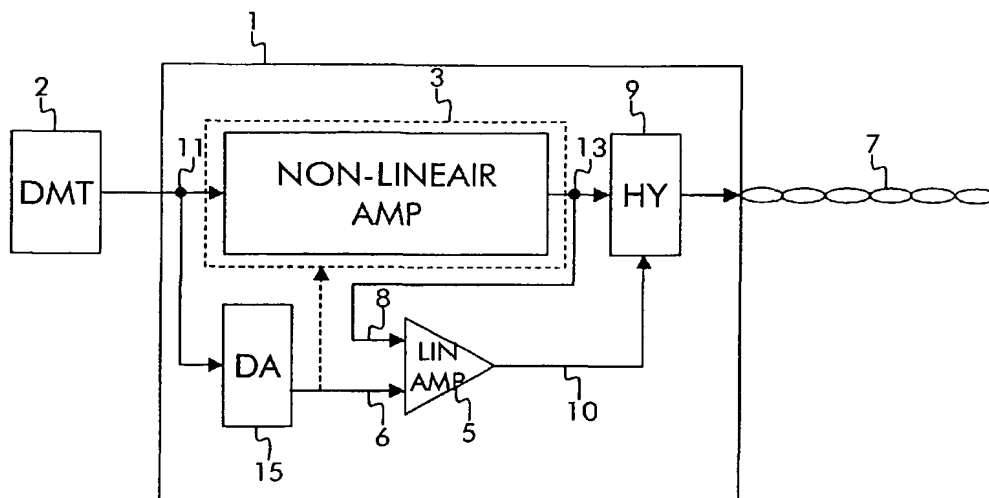


Fig. 2

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## Description

### Field of the invention

[0001] The present invention is related to a novel line driver for applications such as ADSL (Asymmetric Digital Subscriber Line) or VDSL (Very High Speed Digital Subscriber Line).

### State of the art

[0002] In the design of line drivers for applications such as ADSL (Asymmetric Digital Subscriber Line) or VDSL (Very High Speed Digital Subscriber Line), power consumption is a critical issue and signal linearity requirements are stringent. Manufacturers are continually looking for solutions to decrease power consumption. At the moment, the DSL chip market is estimated at 50 million chip sets for 2000 (total value of more than 1 billion USD in 2000).

[0003] A traditional class G driver consumes about 1.3 Watt to produce an output signal of 100 mWatt. The power consumption of the class G driver may be reduced by about  $100 \text{ mWatt}/\eta$  (where  $\eta$  is the efficiency of the amplifier which typically has a value between 0.05 and 0.10) through use of an active back termination. Such an active back terminated line driver is described in EP-A-0901221.

[0004] A class O driver can also be used, including switches and circuitry to monitor the crest factor of the transmitted signal and to appropriately switch between two power supply levels.

[0005] A traditional switch mode driver (SW-DRIVER), which consists of a  $\Sigma\Delta$ -modulator ( $\Sigma\Delta$ ), circuitry (SW) to monitor the crest factor of the transmitted signal and to appropriately switch between different power supply levels, a low pass filter (FI) and a hybrid (HY), less power as digital technology proceeds to deeper submicron technologies. Such a switch mode driver for instance is shown in Fig. 1b of "Basic considerations and topologies of switched-mode assisted linear power amplifiers", IEEE transactions on Industrial Electronics, Vol. 44 No.1 pp 116-123, February 1997. Power consumption is optimized through switching between the different power supply levels. In an improved version of the switch mode driver (SW-DRIVER), the crest factor is monitored in the hybrid (HY). In this way, power consumption is further optimized. The power consumption may even be further reduced by about  $100 \text{ mWatt}/\eta$  ( $\eta$  being the efficiency of the amplifier) through use of active back termination.

[0006] A switch mode amplifier (digital amp.) in parallel with a linear amplifier (analog amp.) each producing part of an outputted audio signal is disclosed in Fig. 4 of "A New High-Efficiency and Super-Fidelity Analog Audio Amplifier with the aid of Digital Switching Amplifier: Class K Amplifier", (IEEE publication, Nam-Sung Jung, Nam-In Kim & Gyu-Hyeong Cho, 1998). In the

class K amplifier, the linear amplifier (analog amp.) is an independent source, whereas the switch mode amplifier (digital amp.) is controlled by the voltage sensed over the resistor  $R_{\text{sense}}$  and consequently is dependent on the current source by the linear amplifier (analog amp.). Also Fig. 2 of "Basic considerations and topologies of switched-mode assisted linear power amplifiers", IEEE transactions on Industrial Electronics, Vol. 44 No.1 pp 116-123, February 1997 and Fig. 3 of "A Design of a 10-W Single-Chip Class D audio amplifier with Very High Efficiency using CMOS Technology", IEEE Transactions on Consumer Electronics, Vol. 45, No. 3, pp 465-473, August 1999 show switched-mode assisted linear amplifiers with a structure and functionality similar to that of the class K amplifier of "A New High-Efficiency and Super-Fidelity Analog Audio Amplifier with the aid of Digital Switching Amplifier: Class K Amplifier", already cited above.

[0007] The switching mode line driver (SW-DRIVER) is the most power efficient line driver, but is very sensitive for power supply variations (low power supply rejection) and clock jitter. This results in errors in the transmitted signal.

### Aims of the invention

[0008] The present invention aims to provide a novel line driver which is linear, stable, and efficient. The output of said driver should be free of errors due to power supply variations and clock jitter.

### Summary of the invention

[0009] The present invention concerns a line driver for amplifying an input signal, said line driver comprising:

- a non-linear amplifier arranged to provide a first output signal amplifying said input signal
- an analogue linear amplifier providing a second output signal based on the difference between the input signal and the first output signal, placed in parallel with said non-linear amplifier and being dependent thereon, and
- combining means arranged to combine said first output signal and said second output signal to provide a total output signal to an output line.

[0010] The present invention equally concerns a line driver for amplifying an input signal, said line driver comprising:

- a first input terminal for receiving said input signal,
- a non-linear amplifier connected to said input terminal and arranged to provide a first output signal at a first output terminal,
- an analogue linear amplifier comprising a second and a third input terminal and a second output terminal, set up as a comparator between said input

signal and said first output signal and arranged to provide a second output signal at said second output terminal, and

- combining means arranged to combine said first output signal and said second output signal to provide a total output signal to an output line.

[0011] In a line driver according to the present invention, the proportion of the first output signal in the total output signal is preferably at least 95%.

[0012] The line driver of the invention can comprise a digital to analogue converter arranged to convert the input signal to an analogue input signal and that said analogue input signal is fed to the second input terminal of the linear amplifier

[0013] Preferably, the linear amplifier is selected from the group consisting of class A and class A/B amplifiers, and the non-linear amplifier is preferably selected from the group consisting of switching mode amplifiers, clipping amplifiers, class B, G or K amplifiers and pulse width modulation amplifiers.

[0014] The combining means can comprise e.g. a hybrid. The input signal can be generated by a DMT.

[0015] In an advantageous embodiment of the present invention, the line driver further comprises an active back termination circuit.

[0016] A second aspect of the present invention is A analogue-digital combined amplifier comprising:

- a non-linear digital amplifier serving as an independent current source
- an analogue linear amplifier serving as a voltage source dependent on said non-linear digital amplifier,

wherein the output of said analogue-digital combined amplifier is a combination of the output of said non-linear digital amplifier and said analogue linear amplifier.

[0017] Also in this aspect, the linear amplifier can be selected from the group consisting of class A and class A/B amplifiers, and the non-linear amplifier is preferably selected from the group consisting of switching mode amplifiers, clipping amplifiers, class B, G or K amplifiers and pulse width modulation amplifiers.

[0018] Another aspect of the present invention is a method for amplifying an input signal, comprising the following steps:

- providing a line driver according to the present invention,
- feeding said line driver at the input terminal with said input signal,
- a first amplifying step, comprising amplifying said input signal with the non-linear amplifier and providing the first output signal at the first output terminal,
- a second amplifying step, performed in parallel with said first amplifying step and comprising a digital to

analogue conversion of the input signal to an analogue input signal, and comparing said analogue input signal with said first output signal using an analogue linear amplifier, providing a second output signal at the second output terminal, and

- a combination step comprising combining said first output signal with said second output signal to obtain a total output signal to an output line.

[0019] Said combination step can be performed using a hybrid, and the input signal can be generated by a DMT.

### Short description of the drawings

[0020] Fig. 1 shows a switching mode line driver as known from the prior art.

[0021] Fig. 2 represents a Class C-AB driver according to the present invention.

[0022] Fig. 3 draws a Class C-AB driver according to a preferred embodiment of the present invention.

### Detailed description of the invention

[0023] The present invention describes a novel line driver, called C-AB driver. It comprises at least an analogue linear amplifier, coupled in parallel with a non-linear line driver and producing only a small part of the output signal power, and can compensate for the errors in the output signal from the non-linear line driver due to power supply variations and clock jitter.

[0024] An example of a class C-AB driver according to the present invention can be seen in fig. 2.

[0025] The class C-AB driver 1 comprises a non-linear amplifier 3, being the independent source, and further comprises an analogue linear amplifier 5 which is controlled to compensate for the non linearity of the non-linear amplifier 3 and thus is dependent.

[0026] According to the present invention, the analogue linear amplifier 5 compares the input signal 11 as sent by the DMT 2 and converted digital to analogue by D/A converter 15 with the output signal 13 at the output of the non-linear amplifier 3. The difference between these signals is compensated for by the analogue linear amplifier 5 and the output of both the non-linear amplifier 3 and the analogue linear amplifier 5 are combined by hybrid 9 and sent on line 7.

[0027] The non-linear amplifier can be any non-linear amplifier. Examples are clipping amplifiers, switching mode amplifiers, class K, G or B amplifiers, pulse width modulators, .... The analogue linear amplifier can be any analogue linear amplifier, such as a class A or class A/B amplifier.

[0028] A 3<sup>rd</sup> order/4<sup>th</sup> order RC or LC filter (FI) is required to limit the band to 1 Mhz if a switching mode driver is used as the non-linear amplifiers.

[0029] It is clear that the set-up of the present invention can be combined with other power-saving tech-

niques, such as active back termination.

#### Description of a preferred embodiment of the invention

[0030] In fig 3, one can see a class C-AB line driver 1 according to a preferred embodiment of the present invention, containing a traditional switch mode line driver 17 (with a  $\Sigma\Delta$ -modulator ( $\Sigma\Delta$ ) 19, circuitry (SW) 21 to switch between different power supply levels, and a low pass filter (FI) 23) that produces about 95% of the required output signal power. Such a switch mode line driver 17 can be seen in fig. 1.

[0031] According to this embodiment, as can be seen in fig. 3, the class C-AB line driver 1 contains, in parallel with the switch mode line driver 17, a class AB amplifier 5 that produces about 5% of the required output signal power. This class AB amplifier 5 thereto compares the signal at the output 13 of the switch mode line driver 17 with the digital to analog converted (15) input signal 11 of the class C-AB line driver 1. The difference between these two signals is compensated for by the class AB amplifier 5. Again, a hybrid (HY) 9 combines the signals produced by the switch mode line driver 17 and the class AB amplifier 5.

[0032] The distortion due to power supply changes and clock jitter in this embodiment is reduced by a factor 5 to 8 in comparison with the traditional switching mode line driver as in fig. 1.

[0033] An additional advantage with the class C-AB line driver according to the invention is that power supply can be simplified compared to the traditional G-class amplifier. A class G amplifier needs GND (ground), +Vcc, +Vcc/2, -Vcc and -Vcc/2 potentials. The non-linear and linear amplifier of the class C-AB line driver according to the invention only need +Vcc, -Vcc and GND connections. DC-de-coupling capacitors, inserted at the output of the non-linear amplifier are useful to avoid power supply asymmetry effects when driving the line with two non-linear amplifiers.

#### Claims

1. Line driver for amplifying an input signal, said line driver comprising :

- a non-linear amplifier (3) arranged to provide a first output signal amplifying said input signal
- an analogue linear amplifier (5) providing a second output signal based on the difference between the input signal and the first output signal, placed in parallel with said non-linear amplifier and being dependent thereon, and
- combining means arranged to combine said first output signal and said second output signal to provide a total output signal to an output line (7).

2. Line driver for amplifying an input signal, said line driver comprising:

- a first input terminal (11) for receiving said input signal,
- a non-linear amplifier (3) connected to said input terminal (11) and arranged to provide a first output signal at a first output terminal (13),
- an analogue linear amplifier (5) comprising a second (6) and a third input terminal (8) and a second output terminal (10), set up as a comparator between said input signal and said first output signal and arranged to provide a second output signal at said second output terminal (10), and
- combining means arranged to combine said first output signal and said second output signal to provide a total output signal to an output line (7).

3. Line driver as in claim 1 or 2, **characterised in that** the proportion of the first output signal in the total output signal is at least 95%.

4. Line driver as in any of the claims 1 to 3, **characterised in that** the line driver comprises a digital to analogue converter (15) arranged to convert the input signal to an analogue input signal and that said analogue input signal is fed to the second input terminal (6) of the linear amplifier (5).

5. Line driver as in any of the claims 1 to 4, **characterised in that** the linear amplifier (5) is selected from the group consisting of class A and class A/B amplifiers.

6. Line driver as in any of the claims 1 to 5, **characterised in that** the non-linear amplifier (3) is selected from the group consisting of switching mode amplifiers, clipping amplifiers, class B, G or K amplifiers and pulse width modulation amplifiers.

7. Line driver as in any of the claims 1 to 6, **characterised in that** the combining means comprise a hybrid (9).

8. Line driver as in any of the claims 1 to 7, **characterised in that** the input signal is generated by a DMT (2).

9. Line driver as in any of the claims 1 to 8, **characterised in that** it further comprises an active back termination circuit.

10. An analogue-digital combined amplifier comprising:

- a non-linear digital amplifier (3) serving as an independent current source

- an analogue linear amplifier (5) serving as a voltage source dependent on said non-linear digital amplifier,

wherein the output of said analogue-digital combined amplifier is a combination of the output of said non-linear digital amplifier and said analogue linear amplifier.

11. Analogue-digital combined amplifier as in claim 10, **characterised in that** the linear amplifier (5) is selected from the group consisting of class A and class A/B amplifiers.
12. Analogue-digital combined amplifier as in claim 10 or 11, **characterised in that** the non-linear amplifier (3) is selected from the group consisting of switching mode amplifiers, clipping amplifiers, class B, G or K amplifiers and pulse width modulation amplifiers.
13. A method for amplifying an input signal, comprising the following steps:
  - providing a line driver (1) such as in claim 1 or 2,
  - feeding said line driver (1) at the input terminal (11) with said input signal,
  - a first amplifying step, comprising amplifying said input signal with the non-linear amplifier (3) and providing the first output signal at the first output terminal (13),
  - a second amplifying step, performed in parallel with said first amplifying step and comprising a digital to analogue conversion of the input signal to an analogue input signal, and comparing said analogue input signal with said first output signal using an analogue linear amplifier (5), providing a second output signal at the second output terminal (10), and
  - a combination step comprising combining said first output signal with said second output signal to obtain a total output signal to an output line (7).
14. The method as in claim 13, **characterised in that** said combination step is performed using a hybrid (9).
15. The method as in claim 13 or 14, **characterised in that** the input signal is generated by a DMT (2).

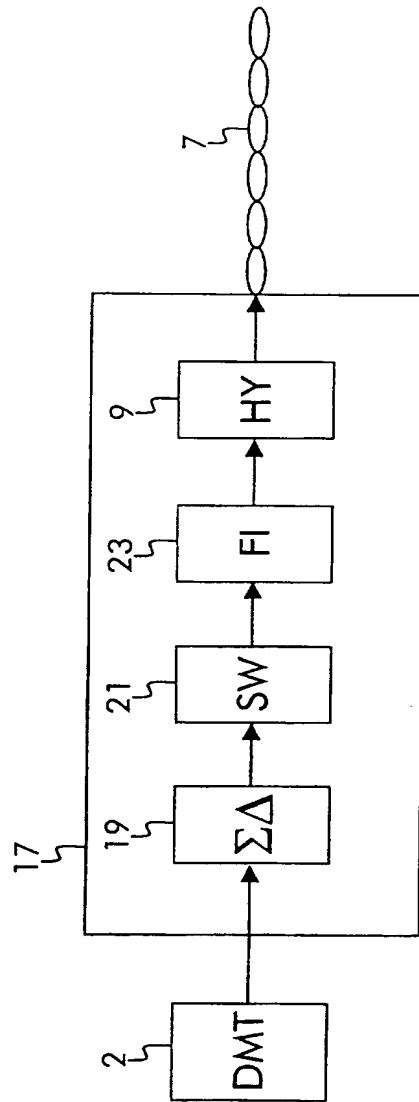


Fig. 1

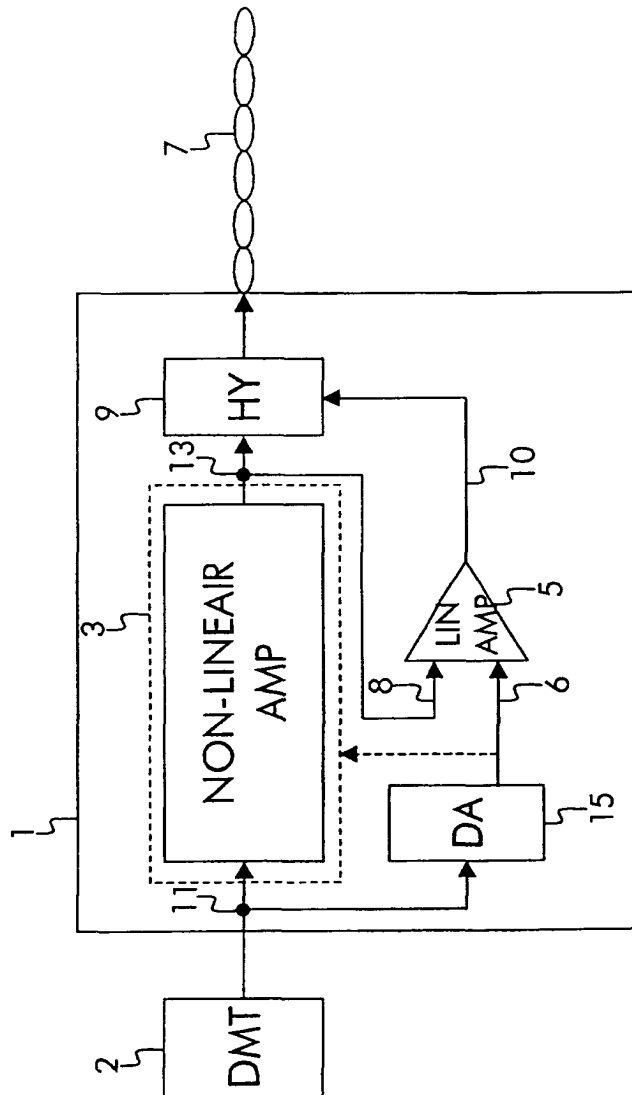


Fig. 2

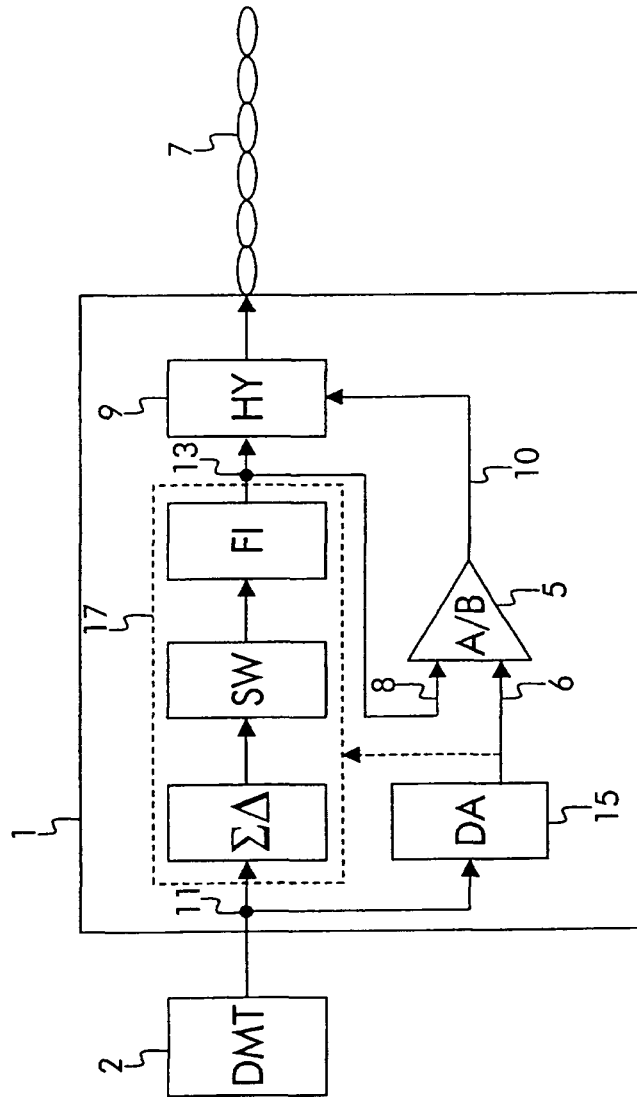


Fig. 3





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Application Number  
EP 00 40 3694

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Place of search		Date of completion of the search	Examiner
THE HAGUE		29 May 2001	Segaert, P
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
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